Fate . . 4

L11	4	independent subarea\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:25
L14	44	compressed command\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:27
L16	3	compressed command sequence\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:52
L17	1	(accessing first subarea near2 memory address array) with (memory bank\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:29
L18	8034	first command\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:30
L19	1	(accessing and ((first subarea near2 memory address array) with (memory bank\$1)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON	2005/10/29 16:29
L20	6269	second command\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON	2005/10/29 16:29
L23	3	114 and (118 with 120)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON	2005/10/29 16:49

L24 .	1	l17 and (l11 or l23)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:31
L25	38	(even clock period\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:49
L27	3	(uneven clock period\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:51
L28	1	I18 and I27	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:51
L29	1	120 and 125	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB		ON	2005/10/29 16:50
L30	17	I7 and (I14 or I18 or I20)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:56
L32	145	(different clock period\$1)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/10/29 16:56